## **SYNTHESIZER**

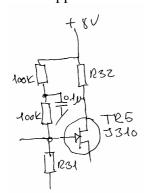
## **Corrections**

## Xx juni 2004

- 1 C4 on the board is C9 on the schematic, C9 is 220nF in the parts list and 10uF 16V in the schematic. There is 24 V across the cap so it must be 10uF 35V.
- 2 C32 on the component side is C37, 100uF 16v.
- 3 Remove track

A new connection made with isolated wire.

- There is an unwelcome connection on the synthesiser pcb between the junction of R4/16/17 and IC3 pin 1.
- 5 C45 should be 18 pF and C46 should be 10 pF (the two value are interchanged).
- R29 (470 ohm) has been paralleled with 220 ohm to lower the output impedance. RFC5 is parallel with 1 kohm to avoid overshooting in the low pass filter.
- 7 It can happen that the PLL won't lock at start up. OZ9FW has solved that this way:



- R53 on the component side is 1K 0.25W 1% metal film.
  R53 on the solder side is 4K7 smd. That one is on the schematic labelled as R58.
- On the solder side D4/C38 and D5/C39 has been interchanged. This is also the case for D8/C49 and D9/C40 (has no practical meaning).